

Roll No.

Total No. of Pages: 02

Total No. of Questions: 07

BCA (Sem. – 2)

COMPUTER SYSTEM ARCHITECTURE

Subject Code: BSBC-204

M Code: 10053

Date of Examination : 20-12-2022

Time: 3 Hrs.

Max. Marks: 60

INSTRUCTIONS TO CANDIDATES:

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

1. Write briefly:

- a) Draw a schematic diagram of the memory hierarchy.
- b) What is Interrupt Cycle?
- c) What do you understand by Hit Ratio?
- d) What is the difference between serial and parallel processing?
- e) What is the need for a Direct Mapping?
- f) Discuss the concept of zero address instruction.
- g) What are the types of Control Unit?
- h) What does locality of reference mean?
- i) What is a DMA controller?
- j) What is the difference between computer organization and computer architecture?

SECTION-B

2. Write short notes on:

a) Associative Mapping

b) Harvard Architecture

c) Handshaking

3. What is the role of input output interface? Discuss isolated input output and memory mapped input output techniques.

4. What do you mean by Control Unit? What are various types of Control Units? What are the major design issues of control unit? Discuss the micro programmed approach of control unit design.

5. Explain Von Neumann Architecture in detail.

6. What is Flynn's classification of computer architecture?

7. Explain SISD, SIMD and MIMD in detail.

NOTE : Disclosure of Identity by writing Mobile No. or Marking of passing request on any paper of Answer Sheet will lead to UMC against the Student.
